

Silicon NPN Power Transistors

BD317

DESCRIPTION

- With TO-3 package
- High DC current gain
- Excellent safe operating area
- Complement to type BD318

APPLICATIONS

- Designed for high power amplifiers

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

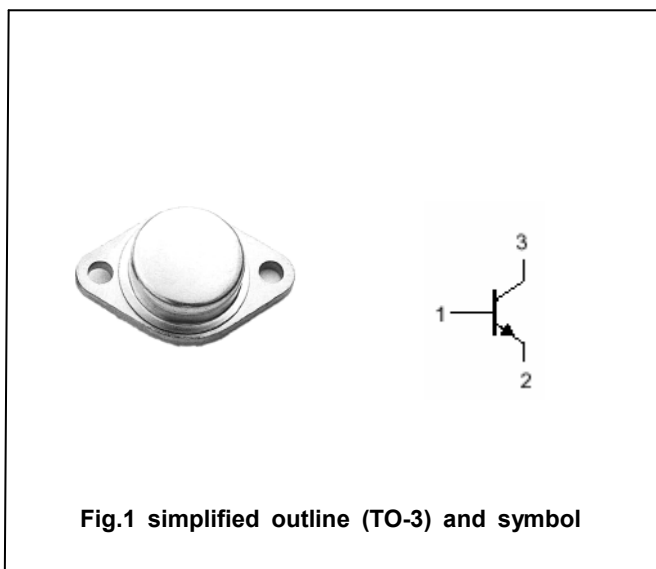


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	100	V
V _{CEO}	Collector-emitter voltage	Open base	100	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		16	A
I _{CM}	Collector current(peak)		20	A
I _B	Base current		5	A
P _D	Total power dissipation	T _C =25°C	200	W
T _j	Junction temperature		-65~200	°C
T _{stg}	Storage temperature		-65~200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal resistance from junction to case	0.875	°C/W

Silicon NPN Power Transistors

BD317

CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.2A ; I _B =0	100			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =8A ; I _B =0.8A			1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =8A ; I _B =0.8A			1.8	V
V _{BE(on)}	Base-emitter on voltage	I _C =8A ; V _{CE} =2.0V			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =100V; I _E =0			1.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			1.0	mA
h _{FE-1}	DC current gain	I _C =5A ; V _{CE} =4V	25			
h _{FE-2}	DC current gain	I _C =10A ; V _{CE} =4V	15			
f _T	Transition frequency	I _C =1A ; V _{CE} =20V, f=0.5MHz	1.0			MHz

PACKAGE OUTLINE

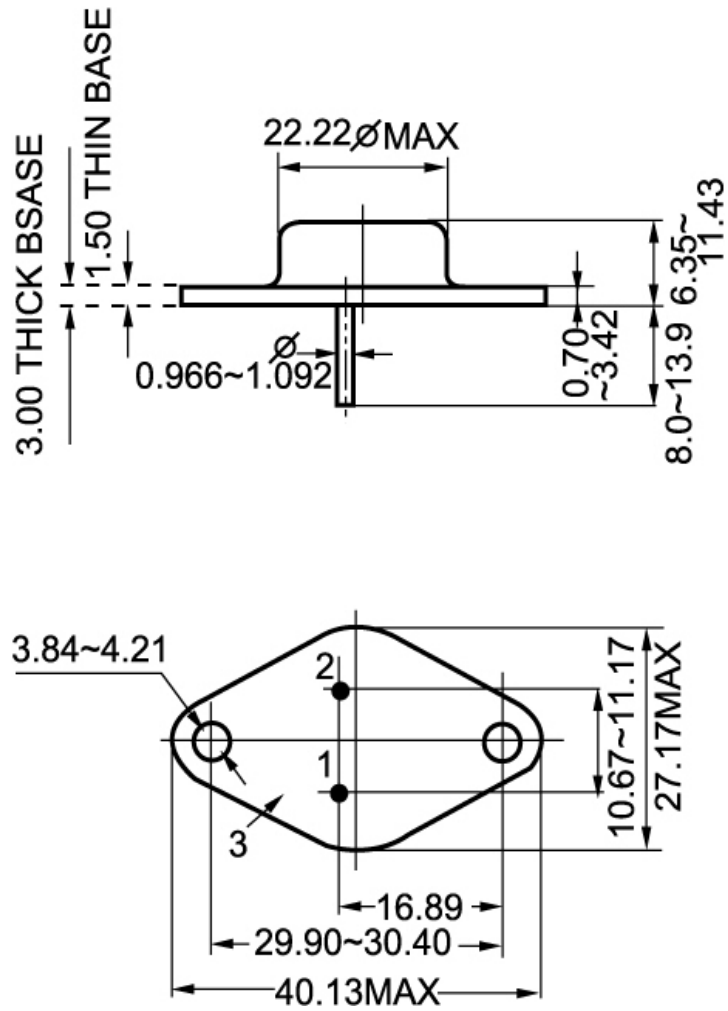


Fig.2 Outline dimensions