

Silicon PNP Power Transistors

2SB1018

**DESCRIPTION**

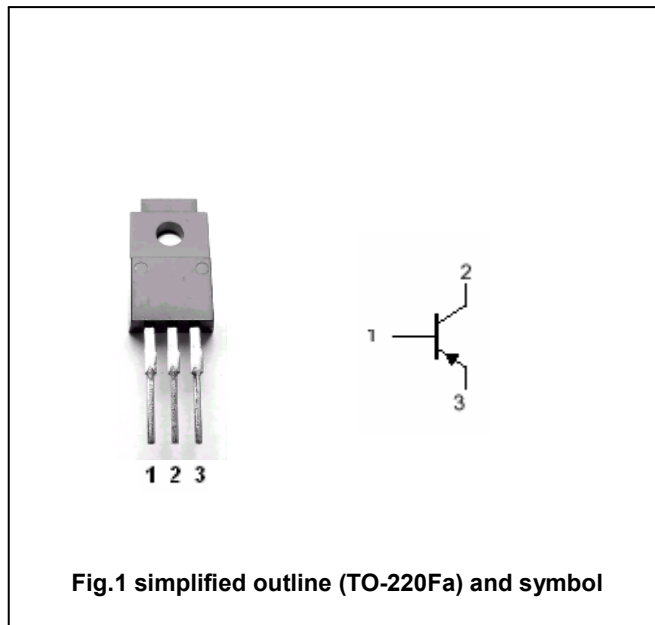
- With TO-220Fa package
- Low saturation voltage
- Complement to type 2SD1411

**APPLICATIONS**

- High current switching applications
- Power amplifier applications

**PINNING**

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



**Absolute maximum ratings(Ta=25°C)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	-100	V
V <sub>CEO</sub>	Collector -emitter voltage	Open base	-80	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	-5	V
I <sub>C</sub>	Collector current		-7	A
I <sub>B</sub>	Base current		-1	A
P <sub>C</sub>	Collector power dissipation	T <sub>a</sub> =25°C	2	W
		T <sub>C</sub> =25°C	30	
T <sub>j</sub>	Junction temperature		150	°C
T <sub>stg</sub>	Storage temperature		-55~150	°C

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =-50mA; I <sub>B</sub> =0	-80			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =-4A ; I <sub>B</sub> =-0.4A		-0.3	-0.5	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =-4A ; I <sub>B</sub> =-0.4A		-0.9	-1.4	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =-100V; I <sub>E</sub> =0			-5	μA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =-5V; I <sub>C</sub> =0			-5	μA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-1V	70		240	
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =-4A ; V <sub>CE</sub> =-1V	30			
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =-1A ; V <sub>CE</sub> =-4V		10		MHz
C <sub>OB</sub>	Output capacitance	I <sub>E</sub> =0 ; V <sub>CB</sub> =-10V; f=1MHz		250		pF

## Switching times

t <sub>on</sub>	Turn-on time	R <sub>L</sub> =10Ω I <sub>B1</sub> =-I <sub>B2</sub> =-0.3A V <sub>CC</sub> =-30V		0.4		μs
t <sub>s</sub>	Storage time			2.5		μs
t <sub>f</sub>	Fall time			0.5		μs

◆ h<sub>FE-1</sub> Classifications

O	Y
70-140	120-240

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PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance:  $\pm 0.15$  mm)