

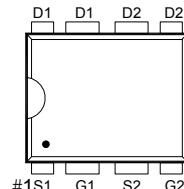
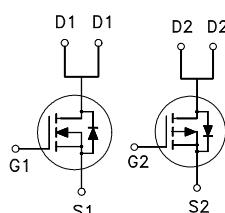
**NIKO-SEM**
**N- & P-Channel Enhancement Mode  
Field Effect Transistor**
**P2503NPG**

DIP-8

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

	$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
N-Channel	30	25m $\Omega$	7A
P-Channel	-30	45m $\Omega$	-5A


G : GATE  
D : DRAIN  
S : SOURCE

100%  $R_g$  tested  
100% UIS tested
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	N-Channel	P-Channel	UNITS
Drain-Source Voltage		$V_{DS}$	30	-30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	7	-5	A
	$T_C = 70^\circ\text{C}$		6	-4	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	20	-20	
Avalanche Current		$I_{AS}$	18	-18	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	19		mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	2.5		W
	$T_C = 70^\circ\text{C}$		1.6		
Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		50	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.<sup>2</sup>Duty cycle  $\leq 1\%$ **ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	N-Ch	30		
		$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$		P-Ch	-30	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	N-Ch	1	1.5	2.5
		$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$		P-Ch	-1	-1.5

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Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$	N-Ch P-Ch			$\pm 100$ $\pm 100$	nA
		$V_{DS} = 0V, V_{GS} = \pm 20V$					
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$	N-Ch P-Ch			1	$\mu A$
		$V_{DS} = -24V, V_{GS} = 0V$				-1	
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ C$	N-Ch P-Ch			10	
		$V_{DS} = -20V, V_{GS} = 0V, T_J = 55^\circ C$				-10	
		$V_{DS} = 5V, V_{GS} = 10V$	N-Ch P-Ch	20			A
On-State Drain Current <sup>1</sup>	$I_{D(ON)}$	$V_{DS} = -5V, V_{GS} = -10V$		-20			
		$V_{GS} = 4.5V, I_D = 6A$	N-Ch P-Ch		25	37	$m\Omega$
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = -4.5V, I_D = -4A$			58	80	
		$V_{GS} = 10V, I_D = 7A$	N-Ch P-Ch		18	25	
		$V_{GS} = -10V, I_D = -5A$			34	45	
		$V_{DS} = 5V, I_D = 7A$	N-Ch P-Ch		19		S
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = -5V, I_D = -5A$			11		

**DYNAMIC**

Input Capacitance	$C_{iss}$	N-Channel $V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$ P-Channel $V_{GS} = 0V, V_{DS} = -10V, f = 1MHz$	N-Ch		790		$pF$
Output Capacitance	$C_{oss}$		N-Ch		175		
Reverse Transfer Capacitance	$C_{rss}$		P-Ch		310		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$	N-Ch		65		
			P-Ch		75		
			N-Ch		2		$\Omega$
			P-Ch		6.25		

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Total Gate Charge <sup>2</sup>	$Q_g$	N-Channel $V_{DS} = 0.5V_{(BR)DSS}$ , $V_{GS} = 10V$ , $I_D = 7A$ P-Channel $V_{DS} = 0.5V_{(BR)DSS}$ , $V_{GS} = -10V$ , $I_D = -5A$	N-Ch		16			nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		N-Ch		14			
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		N-Ch		2.5			
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	N-Channel $V_{DD} = 10V$ $I_D \geq 1A$ , $V_{GS} = 10V$ , $R_{GEN} = 6\Omega$ P-Channel $V_{DD} = -10V$ $I_D \geq -1A$ , $V_{GS} = -10V$ , $R_{GEN} = 6\Omega$	N-Ch		2.2	4.4		ns
Rise Time <sup>2</sup>	$t_r$		N-Ch		6.7	13.4		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		N-Ch		7.5	15		
			P-Ch		9.7	19.4		
Fall Time <sup>2</sup>	$t_f$		N-Ch		11.8	21.3		
			P-Ch		19.8	35.6		
			N-Ch		3.7	7.4		
			P-Ch		12.3	22.2		

**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_J = 25^\circ C$ )**

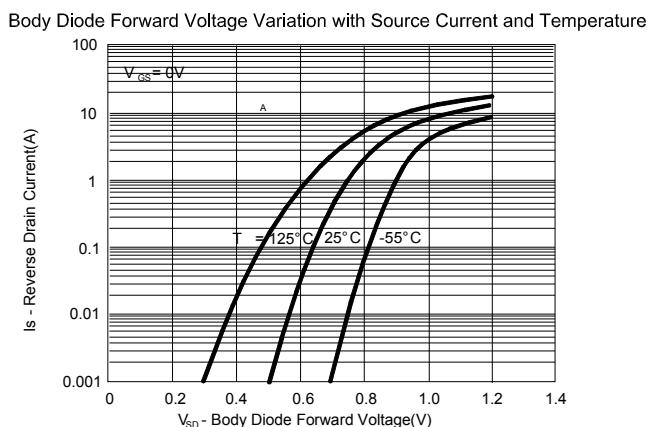
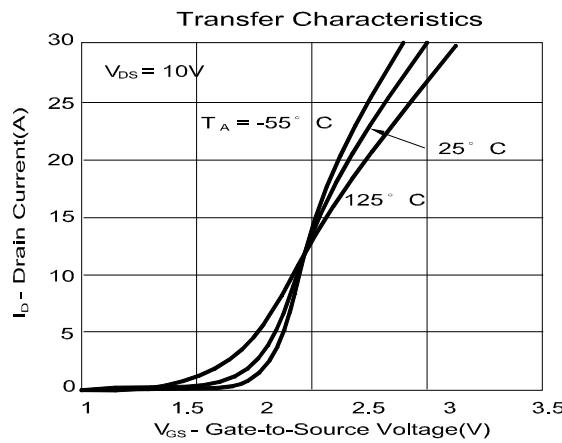
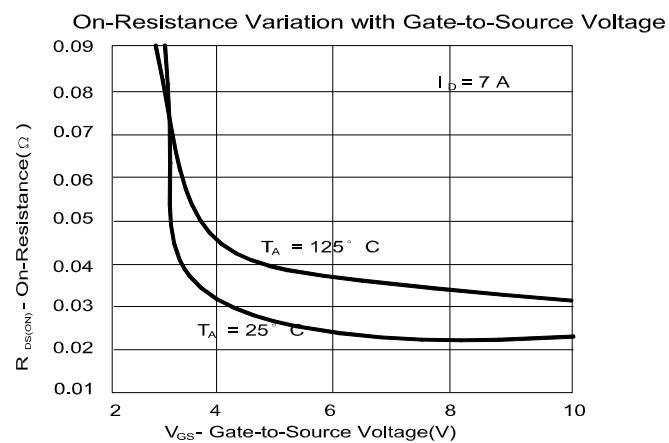
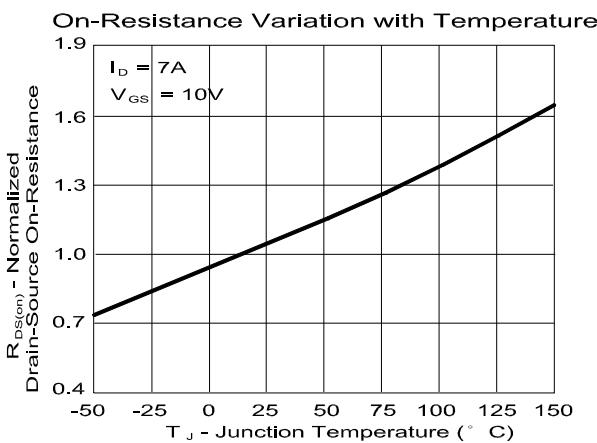
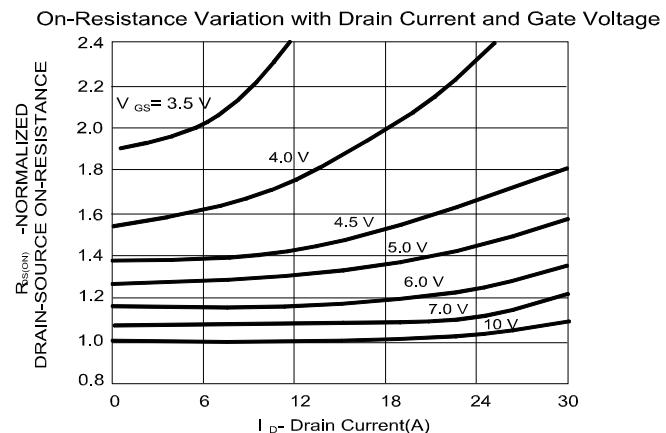
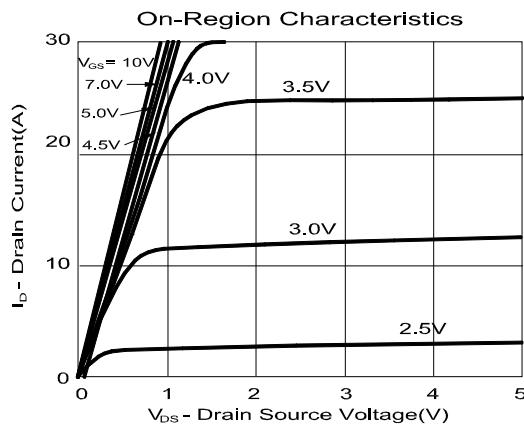
Continuous Current	$I_S$		N-Ch			1.3	A
			P-Ch			-1.3	
Pulsed Current <sup>3</sup>	$I_{SM}$		N-Ch			2.6	V
			P-Ch			-2.6	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 7A$ , $V_{GS} = 0V$	N-Ch			1	V
		$I_F = -5A$ , $V_{GS} = 0V$	P-Ch			-1	

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.<sup>3</sup>Pulse width limited by maximum junction temperature.**REMARK: THE PRODUCT MARKED WITH "P2503NPG", DATE CODE or LOT #**

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**N-CHANNEL**

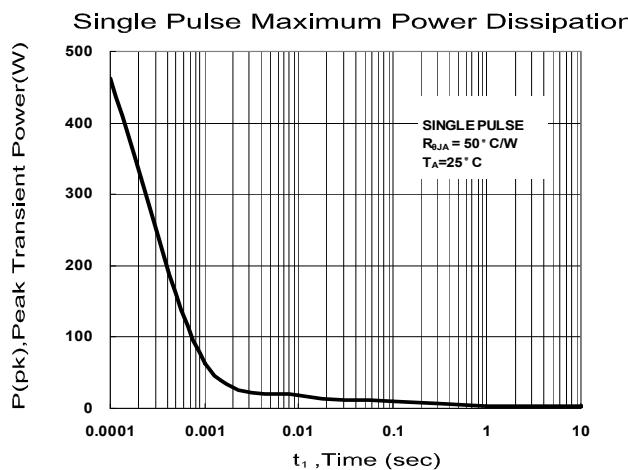
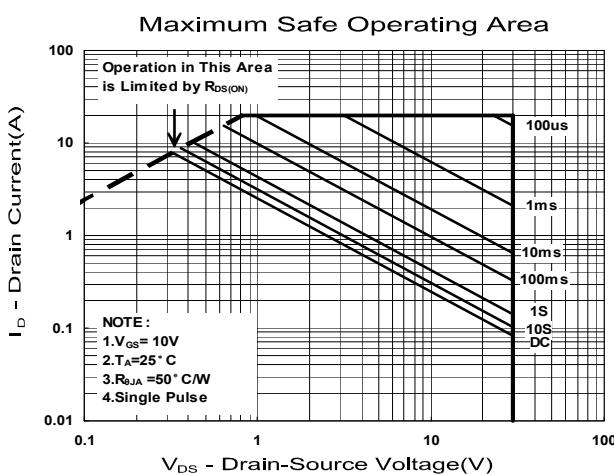
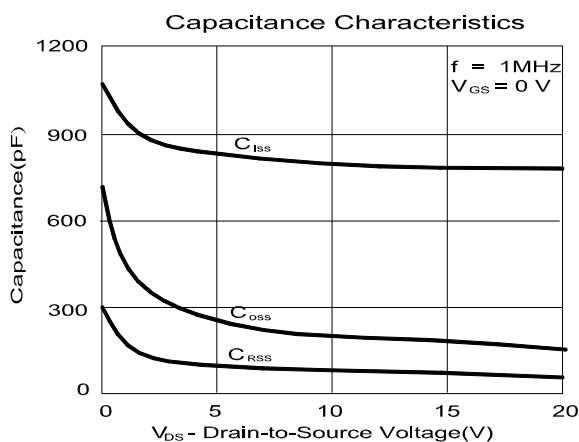
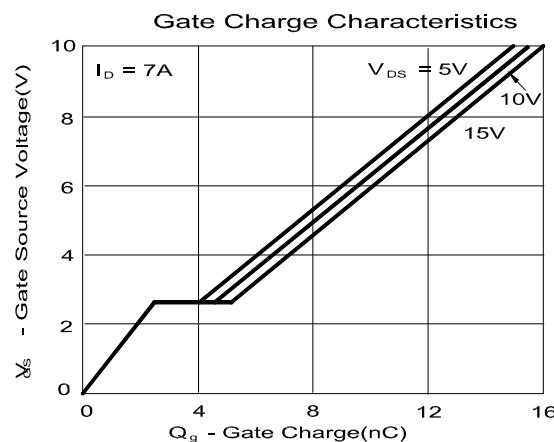
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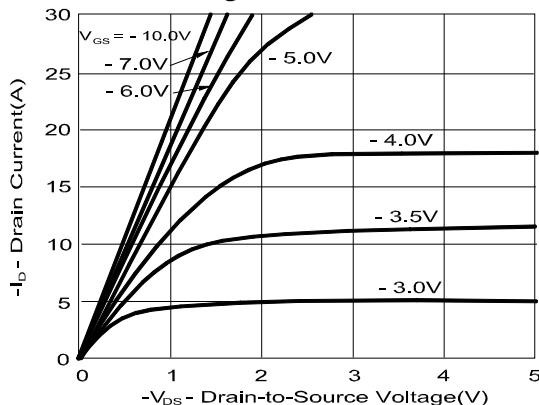
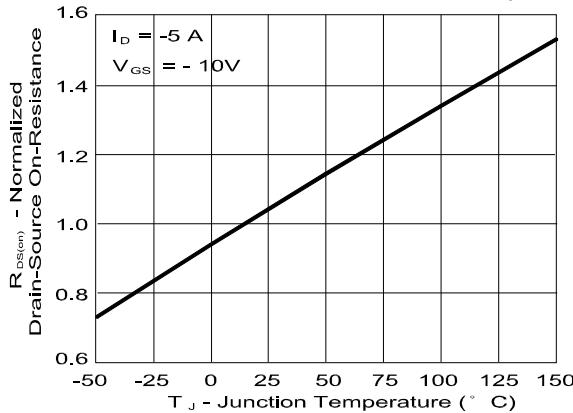
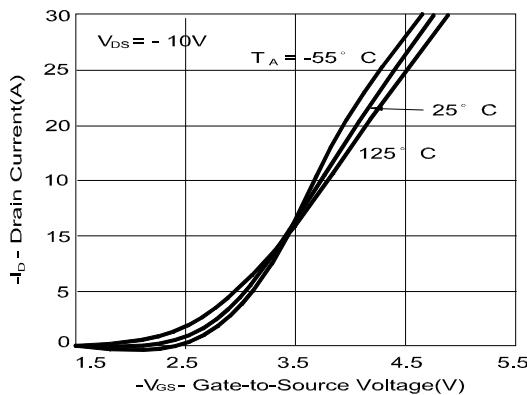
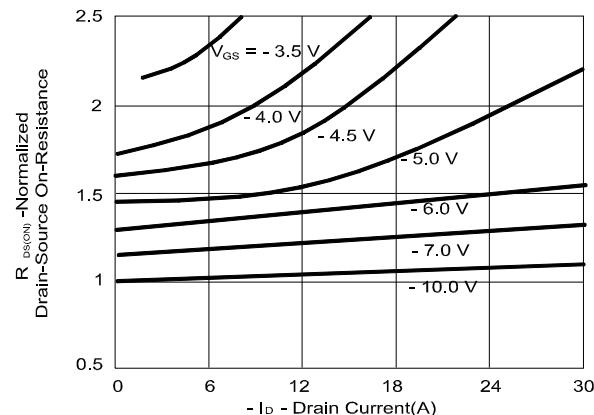
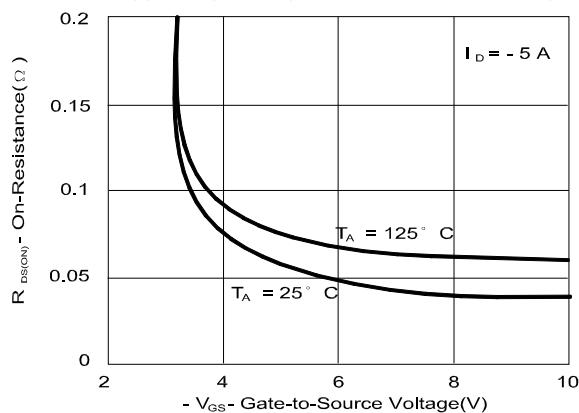
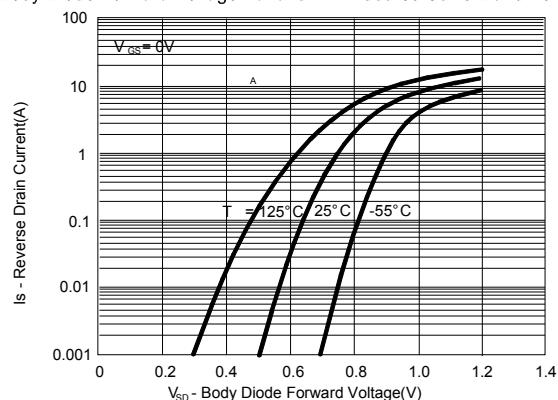
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**P-CHANNEL****On-Region Characteristics****On-Resistance Variation with Temperature****Transfer Characteristics****On-Resistance Variation with Drain Current and Gate Voltage****On-Resistance Variation with Gate-to-Source Voltage****Body Diode Forward Voltage Variation with Source Current and Temperature**

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